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Record 1 of 1**Patent Number(s):** RO127932-A0; RO127932-B1**Title:** System for detecting frames with 3D compatible content, comprises frame receiving circuit connected through bus to a memory unit, memory unit for storing processed data and central processing unit**Inventor Name(s):** BRICK C; COSMA A; DIACONESCU D; MICEA M; MURESAN V; OLAR C**Patent Assignee(s):** MOVIDIUS SRL (MOVI-Non-standard)**Derwent Primary Accession No.:** 2012-Q05565**Abstract:** NOVELTY - The invention relates to a method and a system for analyzing video frames with a view to improving the stereographic image and the video processing mostly used in television and 3D devices. According to the invention, the method consists in applying a filter for detecting input frames, the relevant part of the edge values is used for determining some projections of images on the requested image surfaces, then multiple checkings are performed, each contributing to analyzing the similarity between the processed frames and the characteristics of the frames compatible to some Adjacent 3D frames (SBS) or Overlapped 3D frames (TAB), the checkings consisting in comparing with some specific parameters based on learning from processing an ample range of video frames. The system claimed by the invention consists of a frame receiving circuit (1) functionally connected through a bus to a memory unit (2) meant to store video frames, a memory unit (3) for storing the data processing and a central processing unit (4).**Drawing:****Derwent Class Code(s):** T01 (Digital Computers); W02 (Broadcasting, Radio and Line Transmission Systems); W03 (TV and Broadcast Radio Receivers)**Derwent Manual Code(s):** T01-J10C4; W02-F03B; W03-A12A**IPC:** G06T-015/00; H04N-013/00**Patent Details:**

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