

Record 1 of 1**Title:** P_FENP: A Multiprocessor Real-Time Scheduling Algorithm**Author(s):** Capota, EA (Capota, Eugenia A.); Stangaciu, CS (Stangaciu, Cristina S.); Micea, MV (Micea, Mihai V.); Cretu, VI (Cretu, Vladimir I.)**Book Group Author(s):** IEEE**Source:** 2018 IEEE 12TH INTERNATIONAL SYMPOSIUM ON APPLIED COMPUTATIONAL INTELLIGENCE AND INFORMATICS (SACI) **Pages:** 509-514 **Published:** 2018**Times Cited in Web of Science Core Collection:** 0**Total Times Cited:** 0**Usage Count (Last 180 days):** 0**Usage Count (Since 2013):** 0**Cited Reference Count:** 24**Abstract:** This paper addresses the problem of real-time scheduling on multiprocessor systems for periodic tasks when scheduling jitter is not allowed. A partitioned real-time scheduling method based on a table-driven uniprocessor algorithm called fixed execution non-preemptive (FENP) is proposed as a solution for this problem. An analysis of the new proposed algorithm is provided in terms of scheduling jitter and schedulability ratio, by comparison against other popular partitioned real-time scheduling algorithms.**Accession Number:** WOS:000448144200088**Language:** English**Document Type:** Proceedings Paper**Conference Title:** 12th IEEE International Symposium on Applied Computational Intelligence and Informatics (SACI)**Conference Date:** MAY 17-19, 2018**Conference Location:** Timisoara, ROMANIA**Conference Sponsors:** IEEE**KeyWords Plus:** SYSTEMS**Addresses:** [Capota, Eugenia A.; Stangaciu, Cristina S.; Micea, Mihai V.; Cretu, Vladimir I.] Politehn Univ, Comp & Informat Technol Dept, 2 Vasile Parvan Blvd, Timisoara 300223, Romania.**Reprint Address:** Capota, EA (reprint author), Politehn Univ, Comp & Informat Technol Dept, 2 Vasile Parvan Blvd, Timisoara 300223, Romania.**E-mail Addresses:** eugenia.capota@dsplabs.cs.up.ro; certejan@dsplabs.cs.upt.ro; mihai.micea@cs.upt.ro; vladimir.cretu@cs.upt.ro**Publisher:** IEEE**Publisher Address:** 345 E 47TH ST, NEW YORK, NY 10017 USA**Web of Science Categories:** Computer Science, Artificial Intelligence; Engineering, Electrical & Electronic**Research Areas:** Computer Science; Engineering**IDS Number:** BL1QY**ISBN:** 978-1-5386-4640-3**Source Item Page Count:** 6**Output Date:** 2019-01-07