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Predictable Data Communication Interface for Hard Real-Time Systems

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Book Group Author(s): IEEE

Source: 2008 INTERNATIONAL WORKSHOP ON ROBOTIC AND SENSORS ENVIRONMENTS **Pages:** 98-101 **Published:** 2008

Times Cited: 0 **References:** 16 [Citation Map](#)

Conference Information: IEEE International Workshop on Robotic and Sensors Environment
Ottawa, CANADA, OCT 17-18, 2008
IEEE

Abstract: This paper presents a data communication interface specifically designed to sustain the predictable operation of hard real-time systems. The general interface architecture, data format and communication protocols are discussed along with a case study - the full-duplex SPI (Serial Peripheral Interface) fir the HARETICK kernel. Some of the most interesting experimental results are also presented.

Document Type: Proceedings Paper

Language: English

Author Keywords: Data communication; predictability; hard real-time; SPI; HARETICK

KeyWords Plus: NETWORKS

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Publisher: IEEE, 345 E 47TH ST, NEW YORK, NY 10017 USA

IDS Number: BJB83

ISBN: 978-1-4244-2594-5

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