

**Lista proiecte (Master, Diploma, R&D)**  
**2011 - 2012**

Nr.	Status	Type	General Fields	Project Title	Project Team	Project Management
1	Taken	R&D/ Diploma/ Master	[Software IDE] [Embedded systems] [Real-time systems] [Power Aware]	INVERTA: INtegrated Visual Environment for Real-Time Application Development	4 Students > Anreea-Maria HORVATH (IV CTI) > Silaghi Paul (II CTI) > Sendrea Dumitru (II CTI) > Szever Csaba (II CTI)	Cristina STANGACIU Razvan CIOARGA
				Continues the implementation and the development of the INVERTA integrated visual environment for designing and analyzing real-time applications. Continues the implementation of a real time scheduling simulator by adding among other facilities power-aware real time scheduling support. INVERTA allows the building, specification and visual display of real-time applications, designed as a set of tasks of different types, each task having a characteristic set of parameters (including parameters of time) and a set of control links with other tasks of the application.	R&D Grants "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ) si OPEN-HARTS ( <a href="http://dsplabs.cs.upt.ro/grants/openharts/">http://dsplabs.cs.upt.ro/grants/openharts/</a> ).	
				<b>Descriere proiect</b>  Continuarea si dezvoltarea implementarii mediului vizual integrat INVERTA, destinat proiectarii si analizei aplicatiilor timp-real. Continuarea implementarii unui simulator pentru planificari in sisteme de timp real prin adaugarea de noi facilitati printre care dezvoltarea unui suport pentru planificari de taskuri cu functie de eficientizare a consumului de energie electrica. INVERTA permite construirea, specificarea si afisarea vizuala a unei aplicatii timp-real, conceputa ca set de task-uri de diferite tipuri, fiecare task avand cate un set caracteristic de parametri (inclusiv parametri de timp) si un set de legaturi de control cu celelalte task-uri ale aplicatiei.	<b>Observatii:</b>  R&D Grants "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ) si OPEN-HARTS ( <a href="http://dsplabs.cs.upt.ro/grants/openharts/">http://dsplabs.cs.upt.ro/grants/openharts/</a> ).	
2	Free	R&D	[Software engineering] [Code analysis] [Compiling techniques] [Real-time systems]	Study and development of a Tool for the WCET analysis of real-time applications for the ARM microcontroller.	1 Student: >	Mihai V. MICEA, Cristina STANGACIU
				<b>Project description:</b>  The study of AbsInt Advanced Analyzer, a tool for WCET analysis for ARM7 microcontrollers. Continuation and implementation of a development tool plug-in for the INVERTA environment for WCET analysis for real-time applications tasks written on platforms of type ARM7.	<b>Observations:</b>  R&D Grants "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ) si OPEN-HARTS ( <a href="http://dsplabs.cs.upt.ro/grants/openharts/">http://dsplabs.cs.upt.ro/grants/openharts/</a> ).	
				<b>Descriere proiect</b>  Studiul unui soft pentru analiza WCET-ului (AbsInt Advanced Analyzer) pentru microcontrollore ARM7. Continuarea si dezvoltarea implementarii unui utilitar de tip plug-in pentru mediul INVERTA, destinat analizei timpului WCET pentru taskurile aplicatiilor timp-real scrise pe platforme tip ARM7 TDMI-S.	<b>Observatii:</b>  R&D Grants "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ) si OPEN-HARTS ( <a href="http://dsplabs.cs.upt.ro/grants/openharts/">http://dsplabs.cs.upt.ro/grants/openharts/</a> ).	

Nr.	Status	Type	General Fields	Project Title	Project Team	Project Management
3	Free	R&D/ Diploma	[Embedded systems] [Data acquisition systems] [Real-time systems]	Data acquisition module for the WIT (CORE-TX Wireless Intelligent Terminal)	2 Students: > Simona GHERMAN (III CTI) >	Andrei STANCOVICI, Sinziana INDREICA
			<b>Project description:</b>  The design, implementation and testing of data acquisition mode for WIT. The development of driver software for this module.  It has to use analog-digital converter incorporated in the LPC2000 family of processors, and the possible use of an ATXmega microcontroller family as an acquisition coprocessor.		<b>Observations:</b>  R&D Grant "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> )	
			<b>Project description:</b>  Proiectarea, executarea si testarea modului de achizitii de date pentru WIT. Punerea la punct a driverului soft pentru acest modul.  Se are in vedere utilizarea convertorului analog-numeric incorporat in procesoarele din familia LPC2000, precum si eventuala folosire a unui microcontroler din familia ATXmega pe post de coprocesor de achizitie.		<b>Observations:</b>  R&D Grant "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> )	
4	Partially Taken	R&D	[Wireless sensor networks] [Data fusion and processing] [Embedded systems]	Implementation of the BRAIN (Background Robotic Activity Induction Node), including the activity induction mechanisms	3-4 Students: > Adrian OAIDA (III CTI) > David NICOLA (III CTI) > Daniel NICOLA (III INF)	Razvan CIOARGA, Mihai V. MICEA
			<b>Project description:</b>  The BRAIN module inside the CORE-TX system should suggest various actions and behaviors for the WITS. The behavior should be described as some code which is then transmitted wirelessly to the WIT, which should have some kind of bootloaders and In-Application programming routines to allow its own programming to be changed according to the new behavior. The BRAIN should function as an independent process (like a daemon in linux) which is able to communicate through sockets and has some form of command line interface. Also, the graphical user interface should be implemented using a web server. The BRAIN should have one WIT connected directly to it (USB, serial interface etc) as a gateway interface for the other WITs in the system.		<b>Observations:</b>  R&D Grant "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ).	
			<b>Descriere proiect</b>  Modulul BRAIN din CORE-TX trebuie sa sugereze diverse actiuni si comportamente pentru WIT-uri. Aceste comportamente trebuie sa fie descrise sub forma unui cod care este transmis wireless la WIT; pe acesta trebuie sa ruleze anumite rutine sub forma unui bootloader sau In-Application programming care sa permita alterarea programarii initiale ale WIT-ului in concordanță cu comportamentul primit. Modul BRAIN trebuie implementat ca si un proces independent (similar cu un daemon in Linux) care poate sa comunice prin socketuri si sa contina o forma de interfata prin linie de comanda. Interfata grafica cu utilizatorul trebuie sa fie facuta prin intermediul unui server web. BRAIN trebuie sa fie conectat direct la un WIT (prin USB, interfata seriala etc) pentru a-l folosi pe acesta ca si o interfata de conectare la celelalte WIT-uri.		<b>Observatii:</b>  R&D Grant "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ).	

Nr.	Status	Type	General Fields	Project Title	Project Team	Project Management
5	Free	R&D/ Diploma/ Master	[Robotic systems] [Embedded systems]	Mobility platform/daughterboard for the CORE-TX WIT	2 Students: > >	Dan CHICIUDEAN, Andrei STANCOVICI, Mihai V. MICEA
<b>Project description:</b>					<b>Observations:</b> R&D Grant "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ).	
<b>Descriere proiect</b>					<b>Observatii:</b> R&D Grant "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ).	

Nr.	Status	Type	General Fields	Project Title	Project Team	Project Management
6	Partially Taken	R&D/ Diploma	[Robotic systems] [Embedded systems] [Digital signal processing]	Mobile robot alignment based on ultrasound signals and distance measurements	2 Students: > >	Andrei STANCOVICI, Sinziana INDREICA, Mihai V. MICEA
<b>Project description:</b>						<b>Observations:</b> R&D Grant "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ). Use of the ultrasound modules and the electronic compass for the robot alignment: <a href="http://www.youtube.com/watch?v=ADMgJhLZxt4">http://www.youtube.com/watch?v=ADMgJhLZxt4</a>
<b>Descriere proiect</b>						<b>Observatii:</b> R&D Grant "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ). Utilizarea modulelor ultrasonice si compas electronic pentru alinierea robotilor. <a href="http://www.youtube.com/watch?v=ADMgJhLZxt4">http://www.youtube.com/watch?v=ADMgJhLZxt4</a>

Nr.	Status	Type	General Fields	Project Title	Project Team	Project Management
7	Free	R&D/ Diploma/ Master	[Embedded systems] [Real-time systems] [Wireless communication]	Synchronization in wireless sensor networks and robotic environments	2 Students: > >	Valentin STANGACIU, Mihai V. MICEA
<b>Project description:</b>				<b>Observations:</b> R&D Grant "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ).		
<b>Descriere proiect</b> Sincronizarea ceasului real time intern al nodurilor unei retele de senzori wireless este extrem importanta. Multi algoritmi de rutarea informatiei precum si algoritmi pentru comunicare in timp real in cadrul retelelor de senzori wireless depend de aceasta sincronizare. Acest proiect presupune sincronizarea ceasului in timp real din cadrul sistemului de operare hard real time care ruleaza pe fiecare nod din reteaua de senzori. Sistemul de operare care ruleaza pe fiecare nod este HARETICK iar platforma este ARM7TDMI S. In cadrul proiectului se doreste implementarea unui algoritm de sincronizare a timpului in sisteme embedded in timp real, deja simulat cu success. Implementarea se face pe platforma mai sus mentionata, pe sistemul de operare HARETICK. De asemenea se cere si testarea si evaluarea performantelor algoritmului implementat.				<b>Observatii:</b> R&D Grant "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ).		

Nr.	Status	Type	General Fields	Project Title	Project Team	Project Management
8	Taken	R&D	[Real-time systems] [Communication protocols] [Fieldbus systems]	Multi-Slave FTDMA (Flexible Time Division Multiple Access) implementation of the PARSECS communication system for the WIT	1 Student: > Victor ADASCALITEI (II CTI) > Zsolt BIRO (II CTI)	Mihai V. MICEA
<b>Project description:</b>						<b>Observations:</b> R&D Grant "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ).
<p>PARSECS (Predictable ARchitecture for Sensor Communication Systems) is a real-time communication architecture designed for modular smart sensors, particularly for the WIT intelligent node. The current implementation allows full-duplex communication between a Master board (MotherBoard) and a Slave board (PMBoard). The project aims to interconnect multiple boards within the WIT with the support of the underlying SPI physical interface. Also, the full timeslot paradigm should be implemented and tested.</p> <p>Platform: a WIT prototype exists, consisting of 2 interconnected boards based on the LPC2294 microcontroller (Olimex LPC-H2294 eva-board). This will need to be extended to 3-4 boards.</p> <p>Available HW and SW tools: IDE and compiler (Keil uVision 3.x), debugger (uLINK2 debug tool), 32 channel logic analyzer (LA1032).</p>						<b>Observatii:</b> R&D Grant "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ).
<b>Descriere proiect</b>						
<p>PARSECS (Predictable ARchitecture for Sensor Communication Systems) este o arhitectura de comunicatie in timp real, proiectata pentru senzori inteligenti modulari, in particular pentru nodul inteligent WIT. Implementarea actuala permite comunicatie full-duplex intre o placa Master (MotherBoard) si o placa Slave (PMBoard). Scopul proiectului este sa interconecteze mai multe placi componenete ale WIT-ului, bazat pe interfata fizica SPI. De asemenea, ideea de full timeslot trebuie implementata si testata.</p> <p>Platforma: exista un prototip de WIT, format din 2 placi interconectate, bazate pe microcontrollerul LPC2294 (placa de evaluare Olimex LPC-H2294). Aceasta va trebui extins sa cuprinda 3-4 placi.</p> <p>Unelte HW si SW disponibile: IDE si compilator (Keil uVision 3.x, 4.x), debugger (uLINK2), analizor logic cu 32 canale (LA1032).</p>						

Nr.	Status	Type	General Fields	Project Title	Project Team	Project Management
9	Partially Taken	R&D/ Diploma/ Master	[Robotic collectives] [Emergent behavior] [Robotic movement]	Emergent Movement in Collective Robotic Environments Based on the Study of Ants Movement	1 Student: >	Razvan CIOARGA
				<b>Project description:</b>  Further projects to study emerging movement of robots, using LEGO Mindstorm NXT kits, using emergent behavior patterns inspired by the movement of ants in ant colonies.	<b>Observations:</b>  R&D Grant "MELISSEVS" ( <a href="http://dsplabs.cs.upt.ro/grants/melissevs/">http://dsplabs.cs.upt.ro/grants/melissevs/</a> ).	
				<b>Descriere proiect</b>  Continuarea proiectelor pentru studiul miscarii emergente a robotilor, cu ajutorul kit-urilor LEGO Mindstorm NXT, folosind tipare de comportament emergent preluate din miscarea furnicilor.	<b>Observatii:</b>  R&D Grant "MELISSEVS" ( <a href="http://dsplabs.cs.upt.ro/grants/melissevs/">http://dsplabs.cs.upt.ro/grants/melissevs/</a> ).	
10	Free	R&D/ Diploma/ Master	[Anylogic] [Emergent behavior]	Altering the Anylogic Pedestrian library to allow the simulation of emergent behavior patterns	2 Students: > >	Razvan CIOARGA
				<b>Project description:</b>  Anylogic simulation software has a limited capability of simulating emergent behavior patterns using its Pedestrian library. This should be altered (some of the component classes should be extended) to provide a more efficient way of simulating emergent behavior.	<b>Observations:</b>	
				<b>Descriere proiect</b>  Suitea de simulare Anylogic are posibilitati limitate de simulare a comportamentului emergent prin intermediul bibliotecii Pedestrian. Aceasta trebuie modificaata (o multime de clase componente trebuie extinse) pentru a oferi o modalitate mai eficienta de simulare a comportamentului emergent.	<b>Observatii:</b>	
11	Partially Taken	R&D/ Diploma/ Master	[Android]	Building a command and control interface for the LEGO NXT robots on a Android phone.	2 Students: > >	Razvan CIOARGA
				<b>Project description:</b>  LEGO NXT robots have both Bluetooth and Xbee wireless communication capabilities. This application should present a map of the environment with the location of the robots. For each robot, on click on its icon, a properties / setting menu should appear which should allow the user to set the various parameters of the robots.	<b>Observations:</b>	
				<b>Descriere proiect</b>  Robotii LEGO NXT comunica wireless prin Bluetooth si / sau Xbee. Aplicatia trebuie sa afiseze o hartă a mediului în care se mișca robotii, iar la selectarea unui robot să afiseze un meniu de setări prin care se pot modifica parametrii robotului.	<b>Observatii:</b>	

Nr.	Status	Type	General Fields	Project Title	Project Team	Project Management
12	Taken	Diploma	[Biometric sensors] [Embedded Systems]	Electronic time-keeping system	1 Student: > Anca LUPEI (IV CTI)	Dan CHICIUDEAN, Valentin STANGACIU
			<b>Project description:</b>  Electronic time-keeping system for student activity accounting during laboratory workshop. The accounting is automatically made through a finger-print scanner connected to a computer network. Three major components are involved: the biometric finger print sensor, a embedded system responsible for interfacing biometric sensor to the network and a server for data logging and report generating. The system has the following functionality: - the user comes and presses the finger against the sensor; - the electronic interface gathers data from the sensor and sends it to the server - the server interprets the data and saves it to a database; - a web application also functions on the server: it has time-keeping purposes, attending accounting.	<b>Observations:</b>		
			<b>Descriere proiect</b>  Realizarea unei platforme pentru contabilizarea activitatii pe parcurs in cadrul laboratoarelor. Prezenta la laborator se face automat prin intermediul unui sistem de pontare electronica pe baza de cititor de amprenta digitala. Platforma permite accordarea de calificative/punctaje la fiecare sedinta de laborator pentru fiecare student in parte. Mod de lucru: utilizatorul isi placeaza degetul pe cititorul de amprente, senzorul biometric trimite datele la un microcontroller conectat la o retea LAN, microcontrollerul trimite datele la un server conectat in retea, serverul centralizeaza datele si realizeaza rapoarte.	<b>Observatii:</b>		
13	Free	R&D	[Embedded Systems] [Real time systems] [Real time wireless communication] [Digital signal processing]	Application development on the CORE-TX platform	2 Students: > >	Valentin STANGACIU
			<b>Project description:</b>  Application development on the CORE-TX platform. Adding new functionalities and features to the WIT module and to the real time operating system HARETICK.	<b>Observations:</b>		
			<b>Descriere proiect</b>  Realizarea de aplicatii pe platforma CORE-TX precum si adaugarea de noi functionalitati modulelor existente din WIT precum si dezvoltarea a unor module noi din sistemul de operare in timp real HARETICK.	<b>Observatii:</b>		

Nr.	Status	Type	General Fields	Project Title	Project Team	Project Management
14	Free	R&D/ Diploma	[Power Management] [Rechargeable batteries]	Low-cost and low-power smart charger for Ni-MH rechargeable batteries	2 Students: > >	Gabriel CARSTOIU, Lucian UNGUREAN
<b>Project description:</b>					<b>Observations:</b> References: - PMBoard dimploma project documentation: <a href="http://dsplabs.cs.upt.ro/~cgaby/readarticle.php?article_id=1">http://dsplabs.cs.upt.ro/~cgaby/readarticle.php?article_id=1</a> - diploma project Lucian Ugurean	
<b>Descriere proiect</b>					<b>Observatii:</b> Referinte: - Documentatie diploma <a href="http://dsplabs.cs.upt.ro/~cgaby/readarticle.php?article_id=1">http://dsplabs.cs.upt.ro/~cgaby/readarticle.php?article_id=1</a> - Documentatie proiect diploma Lucian Ungurean	

Nr.	Status	Type	General Fields	Project Title	Project Team	Project Management
15	Taken	R&D/ Master	[Embedded systems] [Real time systems] [Data acquisition systems] [ZigBee protocol]	Ad-Hoc Orienting and Localizing for Indoor Robots (Location management model and simulation)	2 Students: > >	Andrei STANCOVICI Sinziana INDREICA
<b>Project description:</b>				<b>Observations:</b> Dissertation project for Master degree graduation.  R&D Grant "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ). R&D Grant "MELISSEVS" ( <a href="http://dsplabs.cs.upt.ro/grants/melissevs/">http://dsplabs.cs.upt.ro/grants/melissevs/</a> ). Use of the ultrasound modules and Xbee wireless communication module for distance measurement. <a href="http://www.youtube.com/watch?v=ADMgJhLZxt4">http://www.youtube.com/watch?v=ADMgJhLZxt4</a>		
<b>Descriere proiect</b>				<b>Observatii:</b> Proiect pentru disertatie la absolvirea ciclului Master.  R&D Grant "CORE-TX" ( <a href="http://dsplabs.cs.upt.ro/grants/coretx/">http://dsplabs.cs.upt.ro/grants/coretx/</a> ). R&D Grant "MELISSEVS" ( <a href="http://dsplabs.cs.upt.ro/grants/melissevs/">http://dsplabs.cs.upt.ro/grants/melissevs/</a> ). Se vor utiliza modulele ultrasonice achizitionate si modulele de comunicare wireless Xbee. <a href="http://www.youtube.com/watch?v=ADMgJhLZxt4">http://www.youtube.com/watch?v=ADMgJhLZxt4</a>		



Nr.	Status	Type	General Fields	Project Title	Project Team	Project Management
-----	--------	------	----------------	---------------	--------------	--------------------

For further information or if you have any questions, please visit our website or contact us at the following addresses:

- Mihai V. MICEA: [mihai.micea@cs.upt.ro](mailto:mihai.micea@cs.upt.ro)
- Dan CHICIUDEAN: [cdan@dsplabs.cs.upt.ro](mailto:cdan@dsplabs.cs.upt.ro)
- Razvan CIOARGA: [razvanc@dsplabs.cs.upt.ro](mailto:razvanc@dsplabs.cs.upt.ro)
- Boogdan STRATULAT: [bogdan.stratulat@cs.upt.ro](mailto:bogdan.stratulat@cs.upt.ro)
- Gabriel CARSTOIU: [gabriel.carstoiu@gmail.com](mailto:gabriel.carstoiu@gmail.com)
- Lucian UNGUREAN: [luci.ungurean@gmail.com](mailto:luci.ungurean@gmail.com)
- Valentin STANGACIU: [valys@dsplabs.cs.upt.ro](mailto:valys@dsplabs.cs.upt.ro)
- Cristina STANGACIU: [certejan@gmail.com](mailto:certejan@gmail.com)
- Andrei STANCOVICI: [mrandy1986@yahoo.com](mailto:mrandy1986@yahoo.com)
- Adrian OAIDA: [adrian.horia@gmail.com](mailto:adrian.horia@gmail.com)